



**SN 10/621,253**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Sudhakar Kale et al.	Examiner:	Leigh Garbowski
Serial No.:	10/621,253	Group Art Unit:	2825
Filed:	July 14, 2003	Docket:	884.142US2
Title:	STRUCTURAL REGULARITY EXTRACTION AND FLOORPLANNING IN DATAPATH CIRCUITS USING VECTORS		
Assignee:	Intel Corporation	Customer Number:	21186

---

**RESPONSE TO RESTRICTION REQUIREMENT**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

This paper responds to the Restriction Requirement mailed on December 20, 2005.